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Inspec - 1969 to date (INZZ)

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- ☒ Classification codes A: Physics, 2-3

- ➔ Classification codes A: Physics, 4-5
- ➔ Classification codes A: Physics, 6
- ➔ Classification codes A: Physics, 7
- ➔ Classification codes A: Physics, 8
- ➔ Classification codes A: Physics, 9
- ➔ Classification codes B: Electrical & Electronics, 0-5
- ➔ Classification codes B: Electrical & Electronics, 6-9
- ➔ Classification codes C: Computer & Control
- ➔ Classification codes D: Information Technology
- ➔ Classification codes E: Mech., Manufac. & Production Engineering
- ➔ Treatment codes
- ➔ Inspec sub-file
- ➔ Language of publication
- ➔ Publication types

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35/3,K/13 (Item 13 from file: 350) Links

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013370043      \*\*Image available\*\*

WPI Acc No: 2000-541982/200049

XRPX Acc No: N00-400771

**Computer-implemented method in network computer system**

Patent Assignee: MICROSOFT CORP (MICT )

Inventor: BROWN K L; WALKER M J

Number of Countries: 001    Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6067551	A	20000523	US 97971076	A	19971114	200049 B

Priority Applications (No Type Date): US 97971076 A 19971114

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6067551	A	25	G06F-017/30	

**Abstract (Basic):**

...      The user version **identifier** number for at least 1 user is compared to the master **copy** version **identifier** number. The master **copy** and user's local **copy** of a document are processed when a conflict exists between 2 copies based on the...

...      A multi user control **file** in the shared **memory** is created with the master **copy** of the document. A master **copy** version **identifier** number is assigned to the master **copy** of the document. The local **copy** is created to allow editing of the local document on the user's local computer. A unique user version **identifier** number is assigned to each local **copy**. INDEPENDENT CLAIMS are also included for the following...

...Provides multi user editing of a document **saved** on the shared **disk** on a network. Performs detection of conflicts and provides resolution of conflicts after detection. Displays...

...record files on shared server providing for automatic recovery from system failure and providing manual **removal** of a user from the multi user editing environment on the network...

...The figure shows the flowchart for accessing the program module, opening the master **copy** of the document and determining whether a multi-user control file is to be generated...

International Patent Class (Main): **G06F-017/30**

Manual Codes (EPI/S-X): **T01-H03D...**

...**T01-H07C5...**

38/3,K/8 (Item 8 from file: 350) Links  
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*THE APPLICANT*

013379368

WPI Acc No: 2000-551306/200051

XRPX Acc No: N00-407913

**Tracing the operation of processes in a multi-processor system by creating a trace buffer in memory and flushing the buffer to backing store when trace information to be stored would overflow the buffer**

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC )

Inventor: HANSEN O B

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
GB 2345557	A	20000712	GB 99221	A	19990107	200051	B
US 6367036	B1	20020402	US 99292705	A	19990415	200226	

Priority Applications (No Type Date): GB 99221 A 19990107

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
GB 2345557	A		12	G06F-011/00	
US 6367036	B1			G06F-011/00	

... of processes in a multi-processor system by creating a trace buffer in memory and flushing the buffer to backing store when trace information to be stored would overflow the buffer

Abstract (Basic):

... A dynamic link library (DLL) creates a common **buffer** in **memory** into which trace **information** supplied by running processes is stored together with standard header information. Every piece of tracing **information** is **allocated** an area of **buffer** using a single common variable. More than one thread or process are prevented from using the common variable **simultaneously** and if the piece of trace information to be stored would overflow the buffer, the buffer is **flushed** to **disk**. **Flushing** occurs only after all threads currently **writing** to **buffer** have finished **writing**. **Information** from different trace calls can be written to buffer in parallel. More than one buffer...

... **Flushing** allows later examination of the trace information and tracing all the running processes in parallel

...Title Terms: **FLUSH**;

38/3,K/14 (Item 14 from file: 350) Links  
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007835023      \*\*Image available\*\*  
WPI Acc No: 1989-100135/198913  
XRPX Acc No: N89-076312

**Computer system control circuit for peripheral units -  
enables rapid and timely transfer of digital data between  
host computer and memory units**

Patent Assignee: UNISYS CORP (BURS )  
Inventor: GIRIDHAR R; REEVE J T; GIRIDHAR R P  
Number of Countries: 013    Number of Patents: 008  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
WO 8902633	A	19890323	WO 88US3091	A	19880912	198913	B
EP 331720	A	19890913	EP 88908872	A	19880912	198937	
US 4864532	A	19890905	US 8799448	A	19870921	198945	
US 4905184	A	19900227	US 8799447	A	19870921	199015	
JP 2503124	W	19900927				199045	
EP 331720	B1	19931027	EP 88908872	A	19880912	199343	
			WO 88US3091	A	19880912		
DE 3885266	G	19931202	DE 3885266	A	19880912	199349	
			EP 88908872	A	19880912		
			WO 88US3091	A	19880912		
KR 9207949	B1	19920919	WO 88US3091	A	19880912	199409	
			KR 89700922	A	19890522		

Priority Applications (No Type Date): US 8799448 A 19870921; US 8799447 A 19870921

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
WO 8902633	A	E	45		
					Designated States (National): JP KR
					Designated States (Regional): AT BE CH DE FR GB IT LU NL SE
EP 331720	A	E			
					Designated States (Regional): BE DE FR GB IT NL SE
US 4864532	A		14		
EP 331720	B1	E	17	G06F-013/00	Based on patent WO 8902633
					Designated States (Regional): BE DE FR GB IT NL SE
DE 3885266	G			G06F-013/00	Based on patent EP 331720
					Based on patent WO 8902633
KR 9207949	B1			G06F-013/00	

... enables rapid and timely transfer of digital  
data between host computer and memory units

...Abstract (Basic): between the host computer (10) and up to seven  
peripherals such as tape (50) and **disc** (51 to 56) drive units.  
All the peripherals driven via the controller are connected onto...

...The large blocks of digital data being **transferred** to or from the host computer may be temporarily stored in a **segmented RAM buffer memory** (24). An integral microprocessor and protocol controller ensure that the peripherals are accessed at optimum times so that each data **transfer** cycle can be completed rapidly and without waste of **transmission** time...

...Abstract (Equivalent): A control system for permitting multiple **simultaneous** I/O data **transfer** cycles, in a peripheral controller, to be executed between a host computer (10) which initiates I/O data **transfer** commands, and multiple numbers (n) of peripheral terminal units (50-56), the combination characterised in...

...a) a **segmented buffer memory** means (24) having 'n+1' addressable page segments and 'm' addressable word locations in each...

...b) **memory** address means for addressing page **segments** and word locations in said **segmented buffer memory** means (24), said **memory** address means including: (b1) a peripheral address register (26), controlled by an arithmetic logic unit (32) for selecting one of said 'n+1' page segments and 'm' word locations for **data** being **transferred** out of said **buffer** memory means (24) to a selected peripheral terminal unit (50-56) or for data being **transferred** from a selected peripheral terminal unit (50-56) to said buffer memory means (24); (b2...

...selecting one of said 'n+1' page segments and 'm' word locations for data being **transferred** from said host computer (10) to said **buffer memory** means (24) or for **data** being **transferred** from said **buffer memory** means (24) to said host computer (10); (b3) a scratch pad address register (30) for ...

...processor means (20) including said arithmetic logic unit (32) for controlling the execution of data **transfers** and for controlling said peripheral system (50-56), and for controlling said scratch pad address...

...Abstract (Equivalent): A buffer memory in a peripheral controller has **dedicated** page and word location segments for each one of a multiple number of attached peripheral units. Additionally, an auxiliary **segment** provides **memory** for the active status of each one of the multiple number of data **transfer** cycle operations which may be occurring **concurrently** and which status can be accessed at the optimum time so that each initiated data **transfer** cycle can be completed in a time-saving fashion ...

...control is provided for accessing page segments and word locations in order to insert or **remove** data. A special queue segment is available to provide **concurrent** status information for each I/O command initiated by a host computer. (13pp...

...A peripheral controller executes data **transfer** operations between a host computer and a multiple number of separate peripheral terminal units. A specialised buffer **memory** control system provides **dedicated** page-segments for each one of the peripheral terminal units to enable the peripheral controller to **concurrently** manage a multiple number of data **transfer** cycles in an optimum fashion in order to increase the throughput of the data **transfer** operations...

...Title Terms: **TRANSFER**;

38/3,K/4 (Item 4 from file: 350) Links  
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015572569      \*\*Image available\*\*  
WPI Acc No: 2003-634726/200360  
Related WPI Acc No: 2002-480357; 2002-589522  
XRPX Acc No: N03-504778

**Information storage and retrieval system modification  
operation implementation method for use in bank, involves storing  
updated information related to predefined object concurrently  
while implementing system modification request**

Patent Assignee: CABANNES D (CABA-I); DUVILLIER E (DUVI-I)

Inventor: CABANNES D; DUVILLIER E

Number of Countries: 001    Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020073082	A1	20020613	US 2000736039	A	20001212	200360    B
			US 2001990524	A	20011120	

Priority Applications (No Type Date): US 2001990524 A 20011120; US  
2000736039 A 20001212

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20020073082	A1		34	G06F-007/00	CIP of application US 2000736039

**Information storage and retrieval system modification  
operation implementation method for use in bank, involves storing  
updated information related to predefined object concurrently  
while implementing system modification request**

Abstract (Basic):

...            The method involves **storing** updated information related  
to predetermined **object** in **persistent memory** (350)  
**concurrently** while implementing the system modification request  
for suspending **write** access to primary data file.  
...            1) computer program product for modification in information  
**storage** and retrieval system...

...2) information **storage** and retrieval system; and...

...For modifying the operation of information **storage** and retrieval  
system such as relational database management system (RDBMS) for  
maintaining multiple records and...

...Allows mirror data files to be automatically implemented in specific  
information **storage** and retrieval systems without using third  
party software, thereby providing an integrated solution to data  
management. Implements addition/**removal** of mirror data file and  
primary data file without requiring system down time during execution



of operation and without blocking **write** access or data updates to data stored in database. Improves **disk** access speed significantly, as the updated object data are written continuously in a sequential manner...

...The figure shows the schematic block diagram of information **storage** and retrieval system...

...**data** server **cache** (330...

...**persistent** memory (350...

...**disk** pages (352A, 352B

...Title Terms: **STORAGE**;

35/3,K/18 (Item 18 from file: 350) Links  
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007424338      **\*\*Image available\*\***  
WPI Acc No: 1988-058273/198809  
XRPX Acc No: N88-044272

**Memory space allocation method for digital optical  
disc memory - uses segment dedicated to header information,  
characterising segments and their internal data parameters**

Patent Assignee: PICARD M (PICA-I)  
Inventor: PICARD M  
Number of Countries: 004    Number of Patents: 005  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
EP 258104	A	19880302	EP 87401779	A	19870730	198809	B
FR 2602363	A	19880205				198813	
US 4792936	A	19881220	US 8777741	A	19870727	198902	
EP 258104	B	19920422	EP 87401779	A	19870730	199217	
DE 3778454	G	19920527	DE 3778454	A	19870730	199223	
			EP 87401779	A	19870730		

Priority Applications (No Type Date): FR 8611128 A 19860731

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 258104	A	F	11		
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Designated States (Regional): DE GB

US 4792936	A		8		
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EP 258104	B	F	12		
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Designated States (Regional): DE GB

DE 3778454	G		G11B-020/12	Based on patent EP 258104	
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**Memory space allocation method for digital optical  
disc memory...**

**...uses segment dedicated to header information, characterising  
segments and their internal data parameters**

**...Abstract (Basic): The data recording uses a memory  
structure comprised of a header (ET1) containing a version number (HV)  
and a segment count (HNS). A list of segments (HLS) is also contained,  
the segments forming a partitioning of the memory space.  
The header also contains, for each segment, a segment name and a  
current write address parameter...**

**...USE/ADVANTAGE - Improved memory management for ROM digital optical  
discs.**

**...Abstract (Equivalent): Process for recording on a non-erasable  
information carrier in which there is a segmentatation of the  
storage space of said carrier and in which segments (S1, S2)**

make it possible to separately...

...a file and content information of a file, wherein at least one segment (50) is **reserved** for **storing** successive header versions for carrying out an updating of the segmentation of the **storage** space, wherein for defining a new segmentation, a new header version is written following onto...

...information and a header body containing variable information, the preamble containing at least the header **identifier** and the header body containing at least the list of segments associated with said header version and wherein for the **writing** of a new header replacing the current header, when the segment containing the successive header...

...Abstract (Equivalent): The process for recording on a non-**erasable** information carrier e.g. a digital optical **disc** permits an updating of the segmentation of the **storage** space of the carrier. A segmentation is defined by a header containing at least one header **identifier** and a list of segments. The segments form a partition of the **storage** space. For defining a new segmentation, a new header is written at the end of...

...Title Terms: **ALLOCATE**;

International Patent Class (Additional): **G06F-015/40**...

35/3,K/10 (Item 10 from file: 350) Links

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014269410      \*\*Image available\*\*

WPI Acc No: 2002-090108/200212

XRPX Acc No: N02-066357

**Non-hierarchical file sub-system stores byte portions of  
data objects contiguously in segment with round-robin selection of new  
stored objects**

Patent Assignee: INFOLIBRIA INC (INFO-N)

Inventor: MORRIS R J; RABII F

Number of Countries: 096    Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 200193106	A2	20011206	WO 2001US17230	A	20010525	200212 B
US 20020032691	A1	20020314	US 2000207995	P	20000526	200222
			US 2001866383	A	20010525	
AU 200165075	A	20011211	AU 200165075	A	20010525	200225
EP 1358575	A2	20031105	EP 2001939572	A	20010525	200377
			WO 2001US17230	A	20010525	
AU 2001265075	A8	20051013	AU 2001265075	A	20010525	200611

Priority Applications (No Type Date): US 2001866383 A 20010525; US  
2000207995 P 20000526

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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WO 200193106	A2	E	32	G06F-017/30	
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Designated States (National): AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA  
CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN  
IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ  
PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR  
IE IT KE LS LU MC MW MZ NL OA PT SD SE SL SZ TR TZ UG ZW

US 20020032691	A1			G06F-012/00	Provisional application US 2000207995
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AU 200165075	A			G06F-017/30	Based on patent WO 200193106
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EP 1358575	A2	E		G06F-017/30	Based on patent WO 200193106
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Designated States (Regional): DE FR GB

AU 2001265075	A8			G06F-017/30	Based on patent WO 200193106
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Abstract (Basic):

... is part of a Web page home server or page cache server and  
comprises a **disk** drive **allocated** by the server with  
multiple object data partitions containing multiple fixed-length  
segments, plus a meta **disk** partition for **storing**  
sub-system meta data and object meta data. An object directory  
comprises an array of directory blocks each with **pointers** to a  
particular **disk** object space within a segment, data being

retrieved using a hash value of a hierarchical...

... **Data buffers** are **allocated** to the  
**file** sub-system to receive and return data objects sequentially  
in response to requests for objects...

...part hash value representing the data object. When a segment is full a  
data object **overwrites** the oldest data object in the segment...

...Sub-system is for **storing** data objects and is a **disk** file  
structure...

...system performs efficient overhead operations such as garbage collection  
and fault recovery. After a crash, **file** seek or other  
**cache** recovery processes do not need to be run prior to  
restarting the **disk** to restructure the directory

...Title Terms: **STORAGE**;

International Patent Class (Main): **G06F-012/00**...

...**G06F-017/30**

Manual Codes (EPI/S-X): **T01-F05E**...

...**T01-G03**...

...**T01-H03A**...

...**T01-J05B2**...

...**T01-N02A3C**

35/3,K/1 (Item 1 from file: 350) Links

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017377630      \*\*Image available\*\*

WPI Acc No: 2005-701279/200572

Related WPI Acc No: 2003-756070; 2003-756078; 2003-802082; 2004-388056

XRPX Acc No: N05-575454

**Computer for storing data on flash memory, has  
file system configured to store pointers in each physical  
sector of flash memory for indicating next physical sector storing  
valid data in flash memory**

Patent Assignee: MICROSOFT CORP (MICT )

Inventor: AASHEIM J D; KALKMAN J; YANG Y

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20050216653	A1	20050929	US 200287672	A	20020227	200572 B
			US 2002301519	A	20021121	
			US 2005137033	A	20050525	

Priority Applications (No Type Date): US 2002301519 A 20021121; US  
200287672 A 20020227; US 2005137033 A 20050525

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20050216653	A1	36	G06F-012/08		CIP of application US 200287672 Div ex application US 2002301519

**Computer for storing data on flash memory, has  
file system configured to store pointers in each physical  
sector of flash memory for indicating next physical sector storing  
valid data in flash memory**

Abstract (Basic):

...      A computer has a file system configured to store **data** on  
a flash **memory** as a series of linked lists by **storing**  
**pointers** in each physical sector of flash **memory**  
**storing** valid **data**. Each **pointer** indicates the  
next physical sector **storing** valid **data** in the flash  
**memory**.

...      1) method of **storing data** on flash **memory**;  
and...

...based system, programmable consumer electronic device, gaming system,  
multimedia system and other smart devices for **storage** of  
**data** on flash **memory**, hard **disk**, **removable**  
media, network and ethernet...

...Avoids pitfalls of **storing allocation** table in fixed  
region of flash **memory** by using **pointer** for linking

**data** with additional **data** in another **memory** location. Enables usage of **pointers** for detecting power failure events, **storing** transaction logs, providing transaction playback after power failure event, ensuring data integrity after power failure and **storing** data spanning several physical locations. Enables to minimize amount of **memory** needed to track **data** stored in flash **memory** and enables to quickly determine whether to **allocate** more memory for **storing** logical to physical sector address mappings...

...The figure shows the flowchart explaining the method used for tracking **data** on a flash **memory** when the **file** system performs **write** requests...

...Title Terms: **STORAGE**;

International Patent Class (Main): **G06F-012/08**

Manual Codes (EPI/S-X): **T01-H01B3...**

35/3,K/4 (Item 4 from file: 350) Links

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015739886      **\*\*Image available\*\***

WPI Acc No: 2003-802087/200375

Related WPI Acc No: 2003-756076; 2006-239551; 2006-251260

XRPX Acc No: N03-642789

**Data storing method of flash memory,  
involves maintaining counters and pointers for each secondary data  
structure containing predetermined capacity of mapping of logical memory  
address to physical memory address**

Patent Assignee: AASHEIM J D (AASH-I); KALKMAN J (KALK-I); YANG Y (YANG-I);  
MICROSOFT CORP (MICT )

Inventor: AASHEIM J D; KALKMAN J; YANG Y

Number of Countries: 001    Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20030163663	A1	20030828	US 200287251	A	20020227	200375 B
			US 2002301800	A	20021121	
US 7010662	B2	20060307	US 200287251	A	20020227	200618
			US 2002301800	A	20021121	

Priority Applications (No Type Date): US 2002301800 A 20021121; US  
200287251 A 20020227

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 20030163663	A1	37	G06F-012/00	CIP of application US 200287251
US 7010662	B2		G06F-012/10	CIP of application US 200287251

**Data storing method of flash memory,  
involves maintaining counters and pointers for each secondary data  
structure containing predetermined capacity of mapping of logical memory  
address to...**

Abstract (Basic):

... mapping of logical memory address (1004) to physical memory  
address (1002), are maintained in flash **memory**. A master  
**data** structure containing a **pointer** to each of the  
secondary structures, is also maintained. The counters associated with  
each of...

... 2) computer readable medium **storing** data structure  
**allocating** program; and...

...For management of data in **data** source such as flash **memory**  
, hard **disk**, **removable** medium, network, ethernet, used in  
computing system such as personal computer (PC), server computer, hand  
...



...file system to function with different types of data sources, and avoids the pitfalls of **storing** an **allocation** table in a flash **memory** fixed region, and also ensures **data** integrity after a power failure while reducing the memory requirements...

...Title Terms: **STORAGE**;

International Patent Class (Main): **G06F-012/00**...

...**G06F-012/10**

Manual Codes (EPI/S-X): **T01-H01**...

35/3,K/16 (Item 16 from file: 350) Links

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009299222      \*\*Image available\*\*

WPI Acc No: 1992-426631/199252

XRFX Acc No: N94-220680

**Ensuring memory integrity in electronic printing system when  
replacing storage media - copying files for other  
discs to source disc, updating allocation table and  
erasing temporary allocation table**

Patent Assignee: XEROX CORP (XERO )

Inventor: COMPARETTA C; ENZIEN C R; IPPOLITO R A; LATONE J T; SATHI K;  
SMITH M A

Number of Countries: 002    Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 4320518	A	19921111	JP 91246347	A	19910926	199252 B
US 5345581	A	19940906	US 91678925	A	19910401	199435

Priority Applications (No Type Date): US 91678925 A 19910401

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 4320518	A		27	G06F-003/06	
US 5345581	A		29	G06F-011/00	

**Ensuring memory integrity in electronic printing system when  
replacing storage media...**

**...copying files for other discs to source disc,  
updating allocation table and erasing temporary  
allocation table**

**...Abstract (Basic):** The process involves providing a **permanent** processor **identifier** for the system, and copies of it are stored on each of the **disks** to identify the **disks** that belong with the system. **Disc** location data are stored on each of the **disks** identifying the physical location of each **disk** in the memory. In response to a command to boot the system, it is determined whether any one of the **disks** is defective or replaced, by (1) comparing the system processor **identifier** stored on each of the **disks** with the system processor **identifier**, and (2) comparing the physical location of the **disks** in the **memory** with the **disk** location **data** stored on the **disks**.

...

**...When the system processor identifier or the disk location  
for one of the disks does not match with the system processor**

identifier or the disk location data, a list of the files stored on the disks is built and the files are sorted. A temporary file allocation table with the list is built and new locations are allocated to the files on the disks in the temporary allocation table. The files on a source disk are moved to the new locations allocated for the files in the temporary allocation table on the other disks, and the allocation tables respectively associated with the other disks from the temporary allocation table are updated. Files are copied from the other disk to the source disk, and the allocation table associated with the source disk is updated. The temporary allocation table is then erased

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...ADVANTAGE - Minimises time required to install new disc at customer's site

...Title Terms: **STORAGE;**

International Patent Class (Main): **G06F-003/06...**

...**G06F-011/00**

...International Patent Class (Additional): **G06F-012/00**

...Manual Codes (EPI/S-X): **T01-C01...**

...**T01-F05B...**

...**T01-J11**